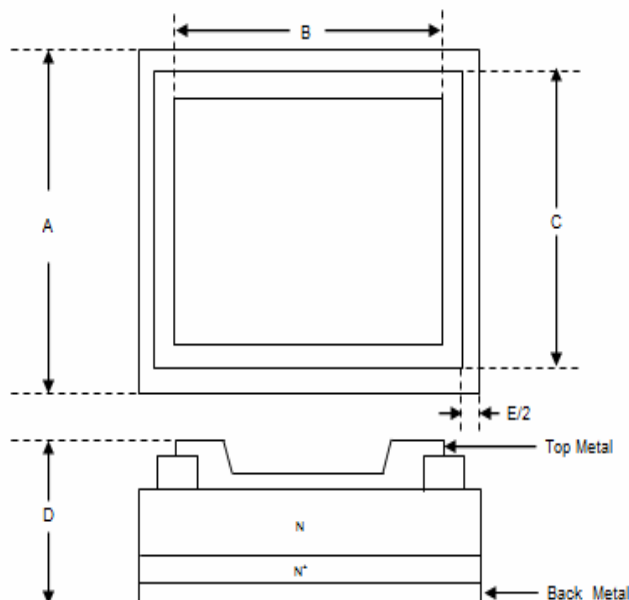


Planar MOS-Controlled Diode Wafer (PMCD)



| Item | Dimensions | |
|------------------------|------------|------|
| | um | mil |
| Die Size (A) | 1573 | 62 |
| Top Metal Pad Size (B) | 1350 | 53.1 |
| Passivation Seal (C) | 1503 | 59.2 |
| Wafer Thickness (D) | 260 | 10.2 |
| Scribe Line Width (E) | 70 | 2.76 |
| Other Informations | | |
| Wafer Size | 6" | |
| Gross Die | 6450 | |
| Top Metal | Ag | |
| Back Metal | Ag | |

Electrical Characteristics @TA=25°C

| Item | Symbol | Spec. Limit | Die Sort | Unit |
|---|-----------|--------------|----------------|---------|
| Maximum Repetitive Peak Reverse Voltage @0.05mA | V_{RRM} | 300 | 315 | V |
| Maximum Average Forward Rectified Current | I_O | 5 | - | A |
| Forward Voltage Drop, @ $I_F=3A$ @ $I_F=5A$ | V_F | 0.87 0.90 | 0.86 - - | V |
| Maximum Reverse Current at Rated V_{RRM} | I_R | 1 | 0.5 | μA |
| Peak Forward Surge Current, 8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC Method) | I_{FSM} | 80 | - | A |
| Operating Temperature Range | T_J | -50 to +175 | - | °C |
| Storage Temperature Range | T_{STG} | -50 to +175 | - | °C |