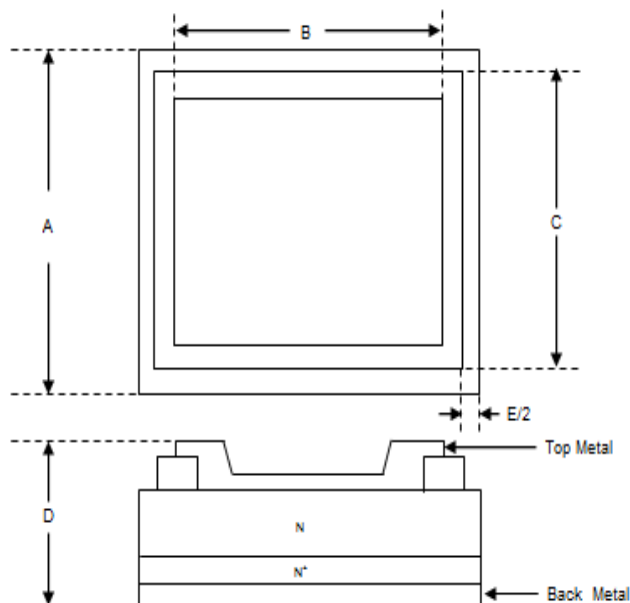


Planar MOS-Controlled Diode Wafer (PMCD)



Item	Dimensions	
	um	mil
Die Size (A)	1486	58
Top Metal Pad Size (B)	1295	51.0
Passivation Seal (C)	1416	55.7
Wafer Thickness (D)	260	10.2
Scribe Line Width (E)	70	2.76
Other Informations		
Wafer Size	6"	
Gross Die	7200	
Top Metal	Ag	
Back Metal	Ag	

Electrical Characteristics @TA=25°C				
Item	Symbol	Spec. Limit	Die Sort	Unit
Maximum Repetitive Peak Reverse Voltage @0.2mA	V_{RRM}	60	64	V
Maximum Average Forward Rectified Current	I_O	5	-	A
Forward Voltage Drop, @ $I_F=3A$ @ $I_F=5A$	V_F	0.49 0.57	0.48 -	V
Maximum Reverse Current at Rated V_{RRM}	I_R	100	70	μA
Peak Forward Surge Current, 8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC Method)	I_{FSM}	80	-	A
Operating Temperature Range	T_J	-50 to +150	-	°C
Storage Temperature Range	T_{STG}	-50 to +150	-	°C